

GATE-2009

Electrical Engineering

GATE (The Graduate Aptitude Test in Engineering) is an All India Examination conducted by the seven Indian Institutes of Technology and the Indian Institute of Science, Bangalore every year.

The GATE 2008 exam will be 3 hours duration and will be held from 09.30 am to 12.30 pm on second Sunday or 10th of February, 2008. Calculator, Cell-phone or any supportive electronic equipment is strictly prohibited during exam.

The GATE paper is totally objective type with 85 questions. Question paper includes one marks and two marks questions. All questions require thorough study of each topic mentioned in the syllabus decided by GATE Examination committee. It also requires constant study and practice of problem solving techniques to find a solution in a specified time frame. Negative marking require 100% accuracy and self confidence to attempt each question at the time of exam.

300 Institutes in India take admission for M.Tech / MS / ME / Integrated PhD through GATE Examination. The eligibility for GATE, broadly is BE, B.Tech or MSc/MCA/AMIE. For other detail please visit any web of IITs.

Organized & Conducted by:

Vinod N Makwana, GATE Qualified Twice,
Lecturer, EE, ADIT.

Guidance and Supported By:

Dr. R.K.Jain, Principal,ADIT
Prof. D.N.Bhatt, HOD,EE,ADIT

Supported by:

Hitendra K Ataliya,
Lab Assit, EE, ADIT.

Expert Lectures Given By:

See table shown below

Objectives of GATE-2008:

In this programme we are planning to encourage, motivate and train our students by improving their basic fundamental knowledge of Electrical Engineering. In which syllabus of number of subjects will be highlighted and conducted like class room teaching at our premises. During this coaching class we include;

1. Study of all topics as per GATE Syllabus will be covered by Vinod Makwana to improve basic fundamental knowledge.
2. An expert lectures will be conducted by senior Teachers of our department.
3. Regular objective type test will be conducted as per GATE Paper.
4. Seminar will be arranged related to recent trend in electrical engineering at regular interval for the creation of their interest.
5. To achieve 100 % result in Gate-08.

Electrical Students Qualified in GATE-2007:

1. **04EE502 Ghanshyam Shah**
2. **03EE547 Jayesh Trivedi**
3. **03EE522 Dixit Shah**
4. **04EE540 Cyril Mathew**

Registration Fees: *Rs.1000/-per student.*

Classes Timing: *Every Saturday between 3:00 pm To 6:00 pm.*

Expert Lectures given by:

Sr. No.	Expert's Name	Designation	Place of work	Topics Covered
1	Dr. Bhavesh.R.Bhalja	Asst. Prof.	EE, ADIT	Power system
2	Prof. Kalpesh J Chudasama	Asst. Prof.	EE, ADIT	Power system
3	Prof. Chintan R Mehta	Asst. Prof.	EE, ADIT	µp
4	Mr. Hardik A Shah	Lecturer	EE, ADIT	Signals & Systems
5	Mr. Yogesh M Makwana	Lecturer	EE, ADIT	Ele. Machines
6	Mr. Sachin R Patel	Lecturer	Maths, ADIT	Maths
7	Mr.Mukesh D patel	Lecturer	Maths, ADIT	Maths

Award / Prize with certificate for students getting percentile as per following:

1. Above 95% Rs.1001/-
2. Between 90% to 95% Rs.801/-
3. Between 80% to 90% Rs.501/-

Certificate will be given to all students.